## Course Assessment - Part A: Your Plan

Your Email *	
Please select your course & name from the drop-down menu. If your course or name are incorrect or missing, contact the Curriculum and Assessment Administrative Assistant, 541–506–6037 or ggilliland@cgcc.edu.	EET 251 – Digital Electronics 1: Programmable Logic Devises – 1094279 – Chris Spengler – Fall 2019
Part A: Your Plan DIRECTIONS 1. Choose three of your course outcomes to assess and report on this term (these will also be used in your Student Course Evaluation survey):	Read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system
Outcome #1 *	
Outcome #2 *	Determine the behavior of basic logic gates (AND, OR, NOT, NAND, NOR XOR, and XNOR) in a circuit.
Outcome #3 *	Use a programmable logic device and hardware definition language to implement a minimized logical expression
Have you completed an assessment for this course prior to this term?	Yes
If yes, are you assessing different outcomes?	Yes
Comments:	
2. To which degree(s) or certificate(s) does your course map? Degree, Certificate, & Program Outcomes	Associate of Applied Science: Electro-Mechanical Technology
Method of Assessment	quizzes and exams
3. What methods will be used to assess individual student understanding of each of these outcomes? (Please be specific.)	
Outcome #1: Method to assess student understanding *	
Outcome #2: Method to assess student understanding *	labs
Outcome #3: Method to assess student understanding *	end of quarter student survey
4. How will you know if you were successful in your efforts to teach this outcome?	quizzes, exams, completion of labs and class/lab attendance
Outcome #1: *	

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successful in your efforts to teach this outcome? *	
Outcome #3: How will you know if you were successful in your efforts to teach this outcome? *	observation of completed labs
5. Instructor Questions: Create two course specific questions to be included on the Student Course Evaluation. Question #1	Do you feel this course has been beneficial to your understanding of Computer Science and Digital Electronics?
Question #2	Were the labs beneficial to your understanding of the courses video lectures and subject matter?
Do you require the names of students who complete the course evaluation survey? (Please note: names will be sent to instructors the Thursday before term ends)	NO
Reminder, when completing Part B, instructors will be asked the following questions: Describe anything you did to assist the institutional effort to support students in improving achievement of the specified criteria for the following Core Learning Outcomes (CLO): 1. CLO#1 – Communication – "Sources and Evidence" and/or "Organization and Presentation" 2. CLO#2 – Critical Thinking/Problem Solving – "Student Position" and/or "Evaluate Potential Solutions" 3. CLO#4 – Cultural Awareness – "Curiosity" (Encouraging our students to "Ask deeper questions about other cultures and seek out answers to these questions") 4. CLO#5 – Community and Environmental Responsibility – "Understanding Global Systems and/or "Applying Knowledge to Contemporary Global Contexts"	
Created <b>19 Sep 2019</b> 11:46:24 AM	Updated <b>23 Sep 2019</b> 8:38:25 AM

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