Course Assessment – Part A: Your Plan #51

Your Email *	
Please select your course & name from the list. Contact Instructional Services if your course or name are incorrect or missing	EET 251 – Pytel
Outcome #1 *	Read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system.
Outcome #2 *	Determine the behavior of basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) in a circuit.
Outcome #3 *	Use a programmable logic device and hardware definition language to implement a minimized logical expression.
Have you completed an assessment for this course prior to this term?	No
If yes, are you assessing different outcomes?	Yes
Comments:	
2. To which degree, certificate or program outcomes do these course outcomes map? Degree, Certificate & Program Outcomes can be found at: http://www.cgcc.edu/curriculum/program-outcomes	• Renewable Energy Technology Certificate (RET)
Outcome #1 Method to assess student understanding *	Students will be given numbers expressed in decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system and asked to interpret and convert between systems.
Outcome #2 Method to assess student understanding *	Students will be given timing diagrams of inputs to basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) and asked to determine the output.
Outcome #3 Method to assess student understanding *	Students will be given a desired function and asked to minimized the expression using Karnaugh mapping and then implement the design on a FPGA.
4. How will you know if you were successful in your efforts to teach this outcome? Outcome #1 *	80% of students can correctly read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system.

How will you know if you were successful in your efforts to teach this outcome?	80% of students can correctly determine the output of basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) in a circuit.

Outcome #2 *

How will you know if you were successful in your efforts to teach this outcome?	80% of students can properly minimize a logical expression and implement it on a FPGA.

Outcome #3 *

#1	Do you feel confident in your ability to read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system?
#2	Do you feel confident in your ability to determine the behavior of basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) in a circuit?

Do you require the names of students who complete the course evaluation survey? *	• No

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