Course Assessment- Part B: Your Results & Analysis #86

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Please select your course & name from the list. Contact Instructional Services if your course or name are incorrect or missing.	EET 251 Digital Electronic 1 - Pytel
Results 1a. Report the outcome achievement data gathered via the assignments, test, etc. you identified in question 3 of your Part A. *	Students were be given numbers expressed in decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system and asked to interpret and convert between systems. Students were given timing diagrams of inputs to basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) and asked to determine the output. Students were given a desired function and asked to minimized the expression using Karnaugh mapping and then implement the design on a FPGA.
1b. Report the percentage of students who mastered each outcome that you identified in question 3 of your Part A. Outcome #1 *	Read, express, and convert between decimal, binary, 2's complement, hex, BCD, Gray's, or octal number system
% of students who successfully achieved the outcome: *	100
Outcome #2 *	Determine the behavior of basic logic gates (AND, OR, NOT, NAND, NOR, XOR, and XNOR) in a circuit.
% of students who successfully achieved the outcome: *	100
Outcome #3 *	Use a programmable logic device and hardware definition language to implement a minimized logical expression.
% of students who successfully achieved the outcome: *	100
Reflect on you assessment results and provide analysis, considering what contributes to student success and/or lack of success. Include feedback from student course evaluations as appropriate. *	Course organization seems to work well to prepare students for Digital 2.
Based on your analysis in the questions above, what course adjustments are warranted (curricular, pedagogical, etc.)?	Happy as is.

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What resources would be required to implement your recommended course adjustments (materials, training, equipment, etc.)? What Budget implications result? *	Currently using 2014 edition of Xilinix ISE software may have to upgrade to updated Vivado in the next couple years.
Were your assessment methods accurate indicators of student learning? Why or why not? Any additional comments?	Yes, very focused objective assessments of comprehension.
(OPTIONAL) Reflect on any adjustments you made from the last assessment of this course and their effectiveness in student achievement of outcomes?	

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